

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of

Bruce B. Doris et al.

Conf. No.: 6189

Serial No.: 10/695,748

Group Art Unit: 2814

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Examiner: T. Le

For: STRUCTURE AND METHOD TO ENHANCE BOTH nFET AND pFET  
PERFORMANCE USING DIFFERENT KINDS OF STRESSED  
LAYERS

Commissioner for Patents  
United States Patent and Trademark Office  
P. O. Box 1450  
Alexandria, Virginia 22313-1450

**AMENDMENT UNDER 37 C. F. R. §1.111**

Sir:

In response to the Office Action mailed April 10, 2006, please amend the  
above-identified application as follows:

Amendments to the claims begin on page 2 of this paper.

Remarks begin on page 6 of this paper.